

**REMARKS/ARGUMENTS**

Claims 1-10 are pending in the application. By this amendment, claim 1 is being amended to improve its form. No new matter is involved.

In Paragraph 2 which begins on page 2 of the Final Office Action, claims 1-10 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent 5,828,467 of Suzuki in view of U.S. Patent 5,929,936 of Arai, et al. In Paragraph 4 on page 5 of the Office Action, claims 1, 3 and 10 are rejected under 35 U.S.C. § 102(e) as being anticipated by Arai, et al. '936. In Paragraph 6 which begins on page 5 of the Office Action, claim 2 is rejected under 35 U.S.C. § 103(a) as being unpatentable over Arai, et al. '936 in view of the knowledge generally available to one of ordinary skill in the art. These rejections, except for the rejection of claim 10 together with claims 1 and 3 as being anticipated by Arai, et al., are essentially a repeat of the rejections set forth in the prior Office Action. In response, Applicants are substantially amending claim 1, the only independent claim in the application, as described below.

Claim 1 defines a noise cancel circuit for removing noise components in a detected radio signal. The circuit includes an interpolation circuit for performing interpolation processing on the detected radio signal, wherein during generation of a pulse noise, a noise portion of the detected radio signal is interpolated by an output signal from the interpolation circuit. As amended herein, the noise cancel circuit further includes "an LPF for eliminating low frequency components of the detected radio signal, an output of the LPF being provided to the interpolation circuit and the interpolation circuit performing an interpolation process on the output from the LPF". Such limitations refer to differences between the present invention and the cited prior art.

Fig. 1 is a block diagram of a noise cancel circuit in accordance with the invention. As described in lines 7-11 of page 4 of the specification, the LPF 3 blocks the sub-signal and the pilot signal by reducing their levels. Therefore, erroneous processing in the interpolation circuit due to sub-signals and pilot signals is prevented, in accordance with the invention.

In view of this and in view of the limitations added to independent claim 1, such claim is submitted to clearly distinguish patentably over the prior art. Neither Suzuki '467 nor Arai, et al. '936, or the attempted combination thereof, shows or suggests such features in accordance with the invention.

Claims 2-10 depend directly or indirectly from claim 1 and contain all of the limitations thereof. Therefore, such claims are also submitted to clearly distinguish patentably over the art.

In conclusion, claims 1-10 are submitted to clearly distinguish patentably over the cited art for the reasons set forth above. Therefore, reconsideration and allowance are respectfully requested.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at the Los Angeles, California telephone number (213) 337-6700 to discuss the steps necessary for placing the application in condition for allowance.

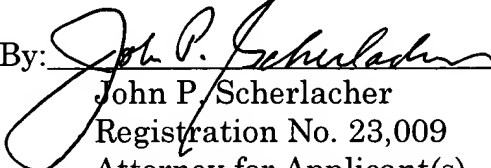
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If there are any fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-1314.

Respectfully submitted,  
HOGAN & HARTSON L.L.P.

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By:   
John P. Scherlacher  
Registration No. 23,009  
Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900  
Los Angeles, California 90071  
Phone: 213-337-6700  
Fax: 213-337-6701